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SERIAL NUMBER 10/718,310	FILING DATE 11/20/2003 RULE	CLASS 257	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. MUH-12876
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APPLICANTS

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** CONTINUING DATA *****

(None)

** FOREIGN APPLICATIONS *****

GERMANY 102 54 160.4 11/20/2002

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 02/18/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY GERMANY	SHEETS DRAWING 4	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 2
Verified and Acknowledged	Examiner's Signature <u>De</u> Initials				

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TITLE

Transistor array and semiconductor memory configuration fabricated with the transistor array

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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